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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	ATTORNEY DOCKET NO. CONFIRMATION NO	
10/781,822 02/20/2004		Ka Lun Choi	1875.1060002/JDE	3963	
75	90 07/12/2004	EXAMINER			
STERNE KES	SSLER GOLDSTEIN &	WELLS, KENNETH B			
1100 NEW YORK AVENUE N W WASHINGTON, DC 20005-3934			ART UNIT	PAPER NUMBER	
***************************************	,, 20 20002 250 .		2816		

DATE MAILED: 07/12/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
Office Action Commons	10/781,822	CHOI, KA LUN				
Office Action Summary	Examiner	Art Unit				
	Kenneth B. Wells	2816				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1) Responsive to communication(s) filed on 201	February 2004.					
	s action is non-final.					
,	3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims						
4)⊠ Claim(s) <u>1-8</u> is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-8</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or election requirement.						
Application Papers						
9)☐ The specification is objected to by the Examiner.						
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
a) ☐ All b) ☐ Some * c) ☐ None of:						
1. Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No.						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau (PCT Rule 17.2(a)).						
* See the attached detailed Office action for a list of the certified copies not received.						
Attachment(s)						
1) Notice of References Cited (PTO-892)	4) Interview Summary					
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  Paper No(s)/Mail Date  Notice of Informal Patent Application (PTO-152)						
Paper No(s)/Mail Date <u>2/20/04</u> .	6) Other:	ateni Application (F 10-102)				
U.S. Patent and Trademark Office						
PTOL-326 (Rev. 1-04) Office A	action Summary Pa	rt of Paper No./Mail Date 20040709				

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1. Claim 8 is objected to because of the following informalities: on line 1, "further comprising including" is grammatically incorrect. Appropriate correction is required.

2. Claims 1-8 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In each of the independent claims, it is misdescriptive, and therefore indefinite, to recite that the second current signal "makes up more than half of the current output" or "is included in the current output". This is because the first and second currents, as understood by the examiner, are the currents through FETs 250 and 252, respectively, and if the recited "current output" is the feedback current from the transconductance cell 204 to the I/O circuit 202 (as shown in instant Fig. 3), then the second current through FETs 248, 252 does not make up more than half of the current output, and is not included in the current output.

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

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A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-8 are rejected under 35 U.S.C. 102(e) as being anticipated by Liang et al.

Note Fig. 4, where the recited input/output circuit reads on all of the charge pump circuitry illustrated in Fig. 4 except for the differential pair of FETs 83, 85 and 87, 89 (which together form the recited transconductance amplifier). voltage control signal of the claims reads on the differential signal made up of signals OP and ON; the recited "input voltage signal" reads on the signal on line 77; the recited first and second circuits of the transconductance amplifier respectively read on differential pairs 83, 85 and 87, 89; the recited current output reads on the currents which flow from the common drains of FETs 83, 85 and 87, 89 to the I/O circuit (these two currents read on the respective first and second currents of the claims); and the voltage difference signal is formed by the UP and DN signals (along with their complements UB and DB). recitation on the last eight lines is deemed to be inherent in the Fig. 4 circuitry of Liang, i.e., the amount of the first and Application/Control Number: 10/781,822 Page 4

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second currents depends on the signal level on line 77. The details of the claimed PLL are illustrated in Liang's Fig. 1.

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kenneth B. Wells whose telephone number is (571)272-1757. The examiner can normally be reached on Monday through Friday from 8:30am to 5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan, can be reached at (571)272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Kenneth B. Wells
Primary Examiner
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